

UNITED STATES PATENT AND TRADEMARK OFFICE
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CATEGORY:

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jc504 U.S. PTO

NEW APPLICATION TRANSMITTAL



12/17/99

Box Patent Application
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Elizabeth A. VanAntwerp
Elizabeth A. VanAntwerp



Transmitted herewith for filing is the patent application of:

Inventor 1: Hans Peter Ostergaard
Address: 5195 Pointe East Drive
Jamesville, NY 13078

Title: A SURFACE MOUNT CROSSOVER COMPONENT

1. Papers enclosed

A. Required for filing date under 37 CFR 1.53(b) (Regular) are:

- 15 Page(s) of Specification
- 5 Page(s) of Claims
- 1 Page(s) of Abstract
- 9 Sheets of Formal Drawings (Figs. 1-10)

B. Other Papers Enclosed

- ☐ A Petition to Accept Photographs as Drawing(s) (37 C.F.R. 1.84(b))
 - ☐ Sheets of Formal Drawings (Figs.) (in triplicate)
- ☐ A Preliminary Amendment
- ☒ A combined Declaration and Power of Attorney
- ☒ An Information Disclosure Statement
- ☒ PTO Form-1449
- ☒ An Assignment Transmittal and Assignment of the invention to Anaren Microwave, Inc.

2. Small Entity Status

- ☒ A statement that this filing is by a small entity is attached.
 - ☐ A separate refund request accompanies this paper.
- ☐ was filed on (original).

3. The filing fee has been calculated as shown below:

- A. Filing Fee
☒ Original Patent Application \$ 380.00
(37 C.F.R. 1.16(a) - \$760.00; Small Entity - \$380.00)
- B. Fees for Claims (16 Claims; 4 Indep.)
☒ each independent claim in excess of 3
(37 C.F.R. 1.16(b) - \$78.00; small entity - \$39.00) \$ 39.00
☐ each claim in excess of 20
(37 C.F.R. 1.16(c) - \$18.00; small entity - \$9.00) \$.00
☐ multiple dependent claim(s)
(37 C.F.R. 1.16(d) - \$260.00; small entity - \$130.00) \$.00
- C. ☒ Assignment Fee \$ 40.00
- Total Fees Due \$ 459.00**

4. Payment of Fees and Authorization to Charge Additional Fees or Credit Overpayment

- ☒ A check in the amount of \$459.00 is enclosed.
- ☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0289. A duplicate copy of this sheet is enclosed.
- ☒ Any additional filing fees required under 37 CFR 1.16.
- ☒ Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,


December 17, 1999

Date

Stephen P. Burr
 Reg. No. 32,970

SPB/eav

WALL MARJAMA BILINSKI & BURR
 101 South Salina Street, Suite 400
 Syracuse, NY 13202
 Telephone: (315) 425-9000
 Facsimile: (315) 425-9114

Customer No.: **20874**

PATENT TRADEMARK OFFICE

Practitioner's Docket No.: 910_001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant or Patentec: Hans Peter Ostergaard

Serial No. or Patent No.: Not Assigned

Filed or Issued: Concurrently Herewith

Title of Invention: A SURFACE MOUNT CROSSOVER COMPONENT

**VERIFIED STATEMENT (DECLARATION) CLAIMING SMALL ENTITY STATUS
(37 CFR 1.9(f) AND 1.27 (c)) - SMALL BUSINESS CONCERN**

I hereby declare that I am

☐ the owner of the small business concern identified below:
☒ an official of the small business concern empowered to act on behalf of the concern identified below:

NAME OF CONCERN: Anaren Microwave, Inc.
ADDRESS OF CONCERN: 6635 Kirkville Road
East Syracuse, NY 13057

I hereby declare that the above identified small business concern qualifies as a small business concern as defined in 13 CFR 121.3-18, and reproduced in 37 CFR 1.9(d), for purposes of paying reduced fees under section 41(a) and (b) of Title 35, United States Code, in that the number of employees of the concern, including those of its affiliates, does not exceed 500 persons. For purposes of this statement, (1) the number of employees of the business concern is the average over the previous fiscal year of the concern of the persons employed on a full-time, part-time or temporary basis during each of the pay periods of the fiscal year, and (2) concerns are affiliates of each other when either, directly or indirectly, one concern controls or has the power to control the other, or a third party or parties controls or has the power to control both.

I hereby declare that rights under contract or law have been conveyed to and remain with the small business concern identified above with regard to the invention, entitled A SURFACE MOUNT CROSSOVER COMPONENT by inventor(s) Hans Peter Ostergaard described in

☒ the specification filed herewith.
☐ application serial no. _____, filed _____.
☐ patent no. _____.

If the rights held by the above identified small business concern are not exclusive, each individual, concern or organization having rights to the invention is listed below* and no rights to the invention are held by any person, other than the inventor, who could not qualify as a small business concern under 37 CFR 1.9(d) or a nonprofit organization under 37 CFR 1.9(e).

*NOTE: Separate verified statements are required from each named person, concern or organization having rights to the invention averring to their status as small entities (37 CFR 1.27).

Full Name: _____

Address: _____

☐ Individual ☐ Small Business Concern ☐ Nonprofit Organization

Full Name: _____

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I acknowledge the duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate (37 CFR 1.28(b)).

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this verified statement is directed.

NAME OF PERSON SIGNING: Larry Sala

TITLE OF PERSON OTHER THAN OWNER: President & CEO

ADDRESS OF PERSON SIGNING: 6635 KIRKVILLE RD.
E. SYRACUSE, NY 13037

SIGNATURE:  DATE: 12-12-99

A SURFACE MOUNT CROSSOVER COMPONENT

Field of the Invention

The present invention relates to a surface mount crossover component for use as a
5 discrete component in an integrated circuit where two signal lines cross one another, and in particular, a surface mount crossover component for use in an integrated circuit where an RF signal line crosses a DC line.

Background of the Invention

10 There are many instances in the design of integrated circuits where signal lines must cross one another. In the field of wireless communications, for example, an integrated circuit includes RF signal lines and DC lines, wherein the RF lines carry the communication signal and the DC line controls and supplies power to solid state components of the circuit, such as semiconductor amplifier components.

15 It is known that the RF signal carried by the RF line is adversely affected by the presence of the DC current carried by the DC line at points where the two lines approach one another, and especially where the two lines cross one another. Accordingly, many attempts have been made to isolate the two lines from one another. One example of isolating the lines when they cross one another is disclosed in U.S. Patent 5,321,375, which
20 describes an RF crossover network that includes an RF line capacitively coupled to an overlying DC line through an interposed dielectric layer. The problem with this crossover network, however, is that the structure of the DC line must be controlled precisely to insure the appropriate capacitive coupling between the DC line and the RF line. Such precise control inevitably makes the device more difficult to manufacture, and consequently
25 increases the cost of production.

Other attempts to isolate RF and DC lines from one another have been incorporated in the design of the overall integrated circuit board. The '375 patent explains that it is common in the design of integrated circuit boards to incorporate multi-layer substrate techniques when building the circuit board itself, wherein the DC lines for the integrated circuit are formed on one dielectric layer, and the RF signal lines are formed on a different dielectric layer to provide electrical isolation between the two lines. It is also common, as explained in the '375 patent, to incorporate a ground plane within the multi-layer integrated circuit board to provide capacitive isolation between the DC lines and the RF signal lines. The '375 patent explains, however, that such multi-layer integrated circuit boards are time consuming and costly to produce, and require more material as a result of the separate ground plane provided within the circuit board between the DC lines and RF signal lines.

Another problem with this conventional technique, however, is that the designer is required to plan and arrange the DC lines for the entire integrated circuit board with respect to the location of the RF signal lines formed on another layer within the integrated circuit board. Accordingly, the layout of the overall circuit board is often dictated by the locations of the DC lines and RF signal lines.

It would be desirable to provide a surface mount crossover component that can be used at discrete locations anywhere on an integrated circuit board where two crossing conductor lines need to be isolated from one another. To date, however, there is no such surface mount crossover component available.

Summary of the Invention

It is an object of the present invention to provide a surface mount crossover component that overcomes the disadvantages of the prior art.

It is another object of the present invention to provide a surface mount crossover component that provides sufficient electrical and capacitive isolation between two crossing conductor lines, and can be manufactured easily and inexpensively.

To achieve these objects, a first embodiment of the present invention is a surface
5 mount crossover component that is used to cross one conductor line over another conductor line that is already printed on an integrated circuit. The surface mount crossover component in accordance with this embodiment comprises a lowermost first dielectric layer, a ground
10 plane layer disposed above the first dielectric layer, a second dielectric layer disposed above the ground plane layer, and a conductor line disposed above the second dielectric layer. The conductor line traverses the second dielectric layer to provide an electrical path from one
15 end of the crossover component to an opposed end thereof. The dielectric layers and the ground plane layer electrically and capacitively isolate, respectively, the conductor lines from one another. Accordingly, current, such as an RF signal, can flow through the component via the conductor line without interference from current, such as a DC control
signal, flowing through the conductor line printed on the integrated circuit.

A second embodiment of the present invention is a surface mount crossover component that is used to cross one conductor line over another conductor line formed in another functional surface mount component. The surface mount crossover component in accordance with the second embodiment comprises a functional surface mount component,
20 a ground plane layer disposed on one of opposite major surfaces of the functional surface mount component, a dielectric layer disposed adjacent the ground plane layer, and a conductor line disposed adjacent the dielectric layer. The conductor line traverses the dielectric layer to provide an electrical path from one end of the crossover component to an

opposed end thereof. The dielectric layer and the ground plane layer electrically and capacitively isolate, respectively, the conductor line from the conductor lines of the functional surface mount component. Accordingly, current can flow through the component via the conductor line without interference from current flowing through the conductor lines of the functional surface mount component.

A third embodiment of the present invention is a surface mount crossover component that is used to cross two conductor lines over one another within the body of the crossover component itself. The surface mount crossover component in accordance with the third embodiment comprises first and second conductor lines that are electrically and capacitively isolated from one another by an interposed ground plane.

A preferred form of the third embodiment includes a bottom ground plane layer, a first dielectric layer disposed above the bottom ground plane layer, a first conductor line disposed above the first dielectric layer, a second dielectric layer disposed above the first conductor line, an internal ground plane layer disposed above the second dielectric layer, a third dielectric layer disposed above the internal ground plane layer, a second conductor line disposed above the third dielectric layer, a fourth dielectric layer disposed above the second conductor line, and a top ground plane layer disposed above the fourth dielectric layer. The surface mount crossover component in accordance with this preferred embodiment of the present invention allows a first signal to be transmitted through the component via one of the first and second conductor lines with no substantial interference from a second signal transmitted via the other one of the first and second conductor lines. As with the other embodiments, the signals passing through the first and second conductor lines, respectively, are electrically and capacitively isolated from one another so that the presence of current passing through one of the conductor lines (e.g., DC current passing through a DC control

line) does not interfere with the signal passing through the other conductor line (e.g., an RF signal passing through an RF line).

As used herein, the phrase "surface mount component" means a component that is to be mounted on a printed, integrated circuit board having printed circuit traces on at least one surface of the board. A surface mount component has termination contacts that are connected to the printed circuit traces by soldered connections between the terminals and the printed circuit traces. Unlike non-surface mount techniques, surface mount components typically do not include leads that extend through holes in the printed circuit board. As such, surface mount components are particularly well suited for automatic assembly.

Surface mount components are mounted on continuous tapes formed into reels that are used by automatic assembly equipment to place the components on the printed circuit board. Typically, the components are temporarily attached to the board with an adhesive, solder paste, or the like prior to soldering, and then soldered in a single operation with the other surface mount components.

These and other features and advantages of the present invention will be better understood from the following detailed description taken in conjunction with the accompanying drawings.

Brief Description of the Drawings

For a fuller understanding of the nature and objects of the invention, reference should be made to the following detailed description of a preferred mode of practicing the invention, read in connection with the accompanying drawings, in which:

Fig. 1 is a top view of a surface mount crossover component in accordance with a first embodiment of the present invention;

Fig. 2 is a cross-sectional view taken through line II-II of Fig. 1;

Fig. 3 is a top view of a surface mount crossover component in accordance with a second embodiment of the present invention;

Fig. 4 is a cross-sectional view taken through line IV-IV of Fig. 3;

Fig. 5 is a top view of a surface mount crossover component in accordance with a third embodiment of the present invention;

Fig. 6 is a cross-sectional view taken through line VI-VI of Fig. 5;

Fig. 7 is a perspective view of the surface mount crossover component shown in Fig.

5;

Fig. 8 is a top view of an alternative form of the surface mount crossover component in accordance with the third embodiment of the present invention;

Fig. 9 is a cross-sectional view taken through line IX-IX of Fig. 8; and

Fig. 10 is an exemplary RF integrated circuit incorporating the surface mount crossover component of the present invention.

Detailed Description of the Invention

Fig. 1 is a top view, and Fig. 2 is a cross-sectional view taken along line II-II of Fig. 1, of a surface mount crossover component 1 in accordance with a first embodiment of the present invention. The component includes a lowermost first dielectric layer 2, a ground plane layer 3 disposed above first dielectric layer 2, a second dielectric layer 4 disposed above ground plane layer 3, and a conductor line 5 disposed above second dielectric layer 4. Conductor line 5 traverses second dielectric layer 4 to provide an electrical path from one end of the crossover component to an opposed end thereof. The overall shape of the component is generally that of a parallelogram, for reasons of manufacturing efficiency. It

is understood, however, that the component can take any shape as long as the crossover function is achieved.

It can be appreciated from Fig. 2 that the surface mount crossover component can be manufactured using known materials (e.g., polymers, ceramics, and the like) and known strip line manufacturing techniques, such as those disclosed in U.S. Patent 4,821,007, the entirety of which is incorporated herein by reference. After all the layers of the component are so formed and laminated, it is preferred to form so-called "half-barrel" termination contacts to allow the component to be soldered to a circuit board using surface mount techniques. Specifically, an input termination contact 6 is formed in electrical communication with one end of conductor line 5, and an output termination contact 7 is formed on the other end, again, in electrical communication with conductor line 5. As is known in the art, it is preferred to bevel the sides of the crossover component to provide reliable electrical contact with conductor line 5. Figs. 1 and 2 show that ground plane layer 3 is recessed away from termination contacts 6 and 7 to insure electrical isolation between the termination contacts and the ground plane layer.

The crossover component of Figs. 1 and 2 can be used anywhere it is necessary to cross one conductor line over another conductor line already printed on a circuit board. For example, the component could be positioned over a printed DC control line, and termination contacts 6 and 7 could be soldered to an RF line that needs to cross the DC control line. The RF signal would flow through the crossover component via conductor line 5 without interference from the DC control signal flowing through the conductor line printed on the circuit, because dielectric layers 2, 4 and ground plane layer 3 electrically and capacitively isolate, respectively, the conductor lines from one another.

It will be apparent to the skilled circuit designer that the impedance of conductor line 5, including its termination contacts 6 and 7, must match the impedance of the system in which the crossover component will be used. For example, a typical impedance for a high power amplifier in a wireless communication system is 50 ohms. The impedance of conductor line 5, carrying the RF signal, is controlled by the dielectric constant and thickness of dielectric layer 4, as well as the cross-sectional dimensions of conductor line 5 itself. As such, it is relatively easy to form conductor line 5 having an impedance of 50 ohms.

The overall shape of the component, as shown in the plan view of Fig. 1, is generally that of a parallelogram, for reasons of manufacturing efficiency. It is understood, however, that the component can take any shape as long as the crossover function is achieved. It is also preferred that the component have an aspect ratio greater than 1 to facilitate correct orientation during automated assembly.

In the event several conductor lines need to cross the conductor line(s) printed on the circuit, a plurality of laterally spaced conductor lines 5 could be formed on dielectric layer 4. Each conductor line would be accompanied by corresponding termination contacts, and appropriate recesses would be formed in ground plane layer 3 to insure electrical isolation, as described above. An alternative design such as this will be explained below with respect to Figs. 8 and 9.

Fig. 3 is a top view, and Fig. 4 is a cross-sectional view taken along line IV-IV of Fig. 3, of a surface mount crossover component 21 in accordance with a second embodiment of the present invention. The component includes a functional surface mount component 22, such as a XINGER® hybrid coupler manufactured by Anaren Microwave, Inc. A ground plane layer 23 is disposed on the upper surface of functional surface mount

component 22 (the ground plane layer 23 is usually formed as a component part of the functional surface mount component). A dielectric layer 24 is disposed above ground plane layer 23, and a conductor line 25 is disposed above dielectric layer 24. Layers 23, 24 and 25 could be formed on the bottom surface of component 22 and achieve the same result.

5 Conductor line 25 traverses dielectric layer 24 to provide an electrical path from one end of the crossover component to an opposed end thereof. Dielectric layer 24 and ground plane layer 23 electrically and capacitively isolate, respectively, the conductor lines from one another. Accordingly, current can flow through the crossover component via conductor line 25 without interference from current flowing through conductor lines of functional surface
10 mount component 22.

As in the first embodiment, an input termination contact 26 is formed in electrical communication with one end of conductor line 25, and an output termination contact 27 is formed on the other end, again, in electrical communication with conductor line 25. Additionally, ground plane layer 23 is recessed away from termination contacts 26 and 27 to
15 insure electrical isolation between the termination contacts and the ground plane layer.

In the event several conductor lines need to cross the conductor line(s) of functional surface mount component 22, a plurality of laterally spaced conductor lines 25 could be formed on dielectric layer 24. Each conductor line would be accompanied by corresponding termination contacts, and appropriate recesses would be formed in ground plane layer 23 to
20 insure electrical isolation, as described above.

Fig. 5 is a top view, Fig. 6 is a cross-sectional view taken through line VI-VI of Fig. 5, and Fig. 7 is a perspective view of a surface mount crossover component in accordance with a third embodiment of the present invention. This surface mount crossover component is used to cross two conductor lines over one another within the body of the crossover

component itself. The surface mount crossover component 30 comprises first 32 and second 33 conductor lines that are capacitively isolated from one another by an interposed ground plane 42. The first conductor line 32 and second conductor line 33 extend along opposite diagonals of the component to allow appropriate spacing between the external termination points of the respective conductor lines. It is understood, however, that the conductor lines could extend from opposite sides of the component and still achieve the intended crossover function.

Fig. 6 is a cross-sectional view taken along line VI-VI of Fig. 5, and shows the individual layers of the surface mount crossover component. The component includes a bottom ground plane layer 40 that is electrically isolated from first conductor line 32 via an interposed first dielectric layer 41. An internal ground plane layer 42 is electrically isolated from first conductor line 32 via a second dielectric layer 43. The internal ground plane layer 42 is also electrically isolated from second conductor line 33 via an interposed dielectric layer 44. A top ground plane layer 45 is electrically isolated from second conductor line 33 via an interposed fourth dielectric layer 46.

The four dielectric layers insure that the first conductor 32, second conductor 33 and ground plane layers 40, 42, 45 are electrically isolated from one another within the body of the crossover component. The internal ground plane layer 42 insures that the first conductor line 32 is also capacitively isolated from the second conductor line 33. In this manner, a first signal (e.g., an RF signal) can be transmitted through the crossover component via first conductor line 32 without encountering any substantial interference from a second signal (e.g., a DC control signal) passing through the crossover component via second conductor line 33.

Fig. 7 is a perspective view of the surface mount crossover component shown in Figs. 5 and 6. It can be appreciated from Fig. 7 that the crossover component is small and compact in size, thus facilitating its discrete use on an integrated circuit board at all locations where two distinct conductor lines cross one another.

As in the case of the other embodiments described above, the surface mount crossover component of Figs. 5-7 can be manufactured using known materials and techniques. In the case of Fig. 6, bottom ground plane layer 40 is formed on the bottom of first dielectric layer 41. The first conductor line 32 can be formed on the top surface of first dielectric layer 41 or the bottom surface of second dielectric layer 43. The same manufacturing options apply to the second conductor line 33. That is, it can be formed on the bottom surface of fourth dielectric layer 46 or the top surface of third dielectric layer 44. Incidentally, while a dotted line is used to show the boundaries between layers 41 and 43, and layers 44 and 46, it is likely that no substantial boundary layer will exist in the finally manufactured component of any embodiment.

The internal ground plane layer 42 can either be formed on the top surface of second dielectric layer 43 or the bottom surface of third dielectric layer 44. It is also possible to form half of the internal ground plane layer 42 on the upper surface of second dielectric layer 43 and the other half on the lower surface of third dielectric layer 44. In this manner, the lower section of the crossover component, which includes layers 40, 41, 32, 43 and one-half of layer 42, and the upper section of the crossover component, which includes layers 45, 46, 33, 44 and one-half of layer 42, could be formed as identical strip line components, and the upper section could be inverted and laminated on the lower section to form the component shown in cross-section in Fig. 6. Of course, one of the upper or lower sections

would have to be rotated 90° to insure that the first conductor line 32 and second conductor line 33 cross each other as shown in Fig. 5.

After all the layers of the component are formed and laminated, a first input termination contact 50 is formed on the upper left-hand beveled corner of the component as shown in Fig. 5, and a first output termination contact 51 is formed on the opposite corner, both in electrical communication with the second conductor line 33. A second input termination contact 52 and a second output termination contact 53 are formed in a similar manner in electrical communication with the first conductor line 32. As is known in the art, it is preferred to bevel the corners of the crossover component to provide reliable electrical contact with the first and second conductor lines 32, 33.

Figs. 5 and 7 also show that the termination contacts extend onto the upper and lower surfaces of the crossover component. The top and bottom ground plane layers 45, 40 are formed in the shape of a cross and are recessed away from those portions of the termination contacts that reside on the top and bottom surfaces of the crossover component. This insures electrical isolation between the top and bottom ground plane layers and the adjacent termination contacts. The orientation and shape of internal ground plane layer 42 is the same as that of top and bottom ground plane layers 45, 40 shown in Figs. 5 and 7. So-called "half-barrel" termination contacts 60 are formed on each side of the crossover component to provide sufficient electrical contact with the ground plane layers, particularly the internal ground plane layer 42.

While the input and output termination contacts shown in Figs. 5 and 7 extend along the entire height of the crossover component, it is possible to form the input and output contacts for the lower, first conductor line 32 to a height that equals the position of the first conductor line within the stacked structure of the crossover component. While such a design

alteration should improve the RF performance of the crossover component, it would require that the component be properly oriented before mounting on an integrated circuit board.

That is, while either the top or the bottom ground plane layer of the component shown in Figs. 5-7 could be mounted to an integrated circuit board, the alternative design described above would require that the bottom ground plane 40 be mounted on the integrated circuit board, so that the RF line, for example, on the integrated circuit board could be soldered, using surface mount techniques, to the input and output termination contacts in communication with the first conductor line 32.

Fig. 8 is a top view, and Fig. 9 is a cross-sectional view taken along line IX-IX of Fig. 8, of an alternative surface mount crossover component 30 in accordance with the third embodiment of the present invention. Like reference numerals designate like components in Figs. 5-9. The surface mount component shown in Figs. 8 and 9 includes a first conductor line 32 and two second conductor lines 33a and 33b. This component can be used when two conductor lines (e.g., RF signal lines) must cross another conductor line (e.g., a DC supply/control line).

Fig. 10 shows an exemplary integrated circuit board that is commonly used in high power amplifiers of wireless communication systems. The circuit board 80 includes an RF signal line 81, which passes through a hybrid coupler 82, such as a XINGER® hybrid coupler manufactured by Anaren Microwave, Inc. The hybrid coupler splits the signal into two signals carried by conductor lines 83 and 84. The signals pass through semiconductor amplifiers 85 and 86, before being re-combined by a second hybrid coupler 87, to form an output signal carried by conductor line 88. Each amplifier 85 and 86 includes a DC supply/control line 89. It can be seen from Fig. 10 that control line 89 must cross RF lines 83 and 84 to reach amplifier 86, for example.

In accordance with the present invention, if DC control line 89 is printed on the circuit board, then surface mount crossover component 1 could be mounted easily to the surface of the integrated circuit board to allow the RF lines 83 and 84 (passing through conductor lines 5) to cross DC control line 89. Two surface mount components could be used, or two conductor lines could be formed on one crossover component 1 to allow both RF lines 83 and 84 to cross line 89 in a single component. Another alternative would be to break the DC control line 89 and use two crossover components 30 to allow RF lines 83 and 84 (passing through conductor lines 33) to cross DC control line 89 (passing through conductor line 32). Yet another alternative would be to run DC control line 89 over hybrid coupler 82 using crossover component 21 shown in Figs. 3 and 4.

It is clear from the above description that the surface mount crossover components of the present invention can be mounted easily to the surface of an integrated circuit board to allow signal lines to cross one another substantially free of interference. In particular, the surface mount crossover components in accordance with the present invention provide an easy and inexpensive solution to the problem of crossing DC control lines and RF signal lines. It is no longer necessary to bury at least one of the lines within the integrated circuit board itself, as discussed in the '375 patent. Accordingly, a substantial cost savings can be realized by using the surface mount crossover component according to the present invention.

The surface mount crossover component of the present invention also provides superior microwave performance when compared to the RF crossover described in the '375 patent, because the RF signal line is capacitively isolated from the DC control line by the presence of an internal ground plane layer interposed between the conductor line carrying the RF signal and the conductor line carrying the DC control signal.

While the present invention has been particularly shown and described with reference to the preferred mode as illustrated in the drawings, it will be understood by one skilled in the art that various changes in detail may be effected therein without departing from the spirit and scope of the invention as defined by the claims.

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We Claim:

1 1. A surface mount crossover component for crossing a conductor line over another
2 conductor line printed on an integrated circuit, comprising:
3 a lowermost first dielectric layer;
4 a ground plane layer disposed above said first dielectric layer;
5 a second dielectric layer disposed above said ground plane layer; and
6 at least one conductor line disposed above said second dielectric layer, said
7 conductor line traversing said second dielectric layer to provide an electrical path from one
8 end of the crossover component to an opposed end thereof;

9 wherein said dielectric layers and said ground plane layer electrically and
10 capacitively isolate, respectively, said conductor line from the conductor line printed on the
11 integrated circuit, such that current flowing through the crossover component via said
12 conductor line encounters no substantial interference from current flowing through the
13 conductor line printed on the integrated circuit.

1 2. The surface mount crossover component of claim 1, further comprising an input
2 termination contact in electrical communication with an input end of said conductor line
3 and an output termination contact in electrical communication with an output end of said
4 conductor line, wherein said ground plane layer is electrically isolated from said termination
5 contacts.

1 3. The surface mount crossover component of claim 1, further comprising a second
2 conductor line disposed above said second dielectric layer spaced laterally from said

conductor line, said second conductor line traversing said second dielectric layer to provide a second electrical path from one end of the crossover component to an opposed end thereof.

4. The surface mount crossover component of claim 3, further comprising a first input termination contact in electrical communication with an input end of said conductor line, a first output termination contact in electrical communication with an output end of said conductor line, a second input termination contact in electrical communication with an input end of said second conductor line, and a second output termination contact in electrical communication with an output end of said second conductor line, wherein said ground plane layer is electrically isolated from said termination contacts.

5. A surface mount crossover component, comprising:
a functional surface mount component including a first conductor line;
a ground plane layer disposed on one of opposite major surfaces of said functional surface mount component;
a dielectric layer disposed adjacent said ground plane layer; and
at least one second conductor line disposed adjacent said dielectric layer, said second conductor line traversing said dielectric layer to provide an electrical path from one end of the crossover component to an opposed end thereof;
wherein said dielectric layer and said ground plane layer electrically and capacitively isolate, respectively, said first and second conductor lines from one another, such that current flowing through the crossover component via said second conductor line

12 encounters no substantial interference from current flowing through said first conductor
13 line.

1 6. The surface mount crossover component of claim 5, further comprising an input
2 termination contact in electrical communication with an input end of said second conductor
3 line and an output termination contact in electrical communication with an output end of
4 said second conductor line, wherein said ground plane layer is electrically isolated from said
5 termination contacts.

1 7. The surface mount crossover component of claim 5, further comprising a third
2 conductor line disposed adjacent said dielectric layer spaced laterally from said second
3 conductor line, said third conductor line traversing said dielectric layer to provide a second
4 electrical path from one end of the crossover component to an opposed end thereof.

1 8. The surface mount crossover component of claim 7, further comprising a first input
2 termination contact in electrical communication with an input end of said second conductor
3 line, a first output termination contact in electrical communication with an output end of
4 said second conductor line, a second input termination contact in electrical communication
5 with an input end of said third conductor line, and a second output termination contact in
6 electrical communication with an output end of said third conductor line, wherein said
7 ground plane layer is electrically isolated from said termination contacts.

1 9. A surface mount crossover component comprising first and second conductor lines
2 electrically isolated from one another by an interposed dielectric layer and capacitively

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isolated from one another by an interposed ground plane layer, wherein current flowing through the crossover component via one of said conductor lines encounters no substantial interference from current flowing through the other one of said conductor lines.

10. The surface mount crossover component of claim 9, wherein said first conductor line extends in a first direction within a first plane, said second conductor line extends in a second direction within a second plane, and said first direction crosses said second direction.

11. The surface mount crossover component of claim 10, wherein said first direction crosses said second direction at an angle of about 90° .

12. A surface mount crossover component, comprising:

- a bottom ground plane layer;
- a first dielectric layer disposed above said bottom ground plane layer;
- at least one first conductor line disposed above said first dielectric layer;
- a second dielectric layer disposed above said first conductor line;
- an internal ground plane layer disposed above said second dielectric layer;
- a third dielectric layer disposed above said internal ground plane layer;
- at least one second conductor line disposed above said third dielectric layer;
- a fourth dielectric layer disposed above said second conductor line; and
- a top ground plane layer disposed above said fourth dielectric layer;

wherein a current flowing through said surface mount crossover component via one of said first and second conductor lines encounters no substantial interference from current flowing through the other one of said first and second conductor lines.

13. The surface mount crossover component of claim 12, wherein said first conductor line extends along a first direction and said second conductor line extends along a second direction crossing said first direction.

14. The surface mount crossover component of claim 12, further comprising a first input termination contact in electrical communication with an input end of said first conductor line, a first output termination contact in electrical communication with an output end of said first conductor line, a second input termination contact in electrical communication with an input end of said second conductor line, and a second output termination contact in electrical communication with an output end of said second conductor line.

15. A surface mount crossover component of claim 14, wherein said component is generally the shape of a parallelepiped with said first input and output termination contacts arranged along one pair of diagonally opposed corners of the crossover component, and the second input and output termination contacts arranged along the other pair of diagonally opposed corners of the crossover component.

16. The surface mount crossover component of claim 14, wherein said ground plane layers are electrically isolated from said termination contacts.

Abstract:

A surface mount crossover component comprising first and second conductor lines electrically isolated from one another by an interposed dielectric layer and capacitively isolated from one another by an interposed ground plane layer. Current flowing through the crossover component via one of the conductor lines encounters no substantial interference from current flowing through the other one of the conductor lines.

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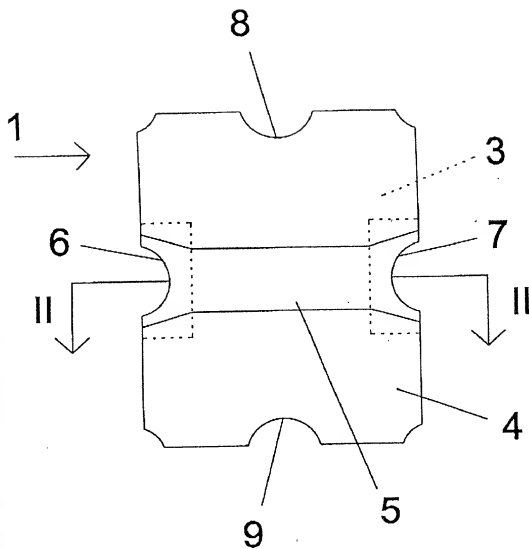


Figure 1

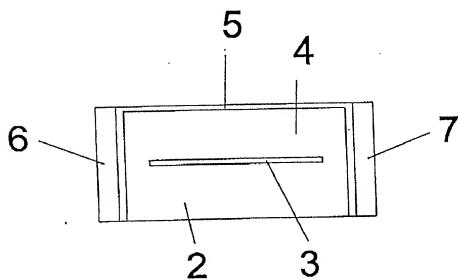


Figure 2

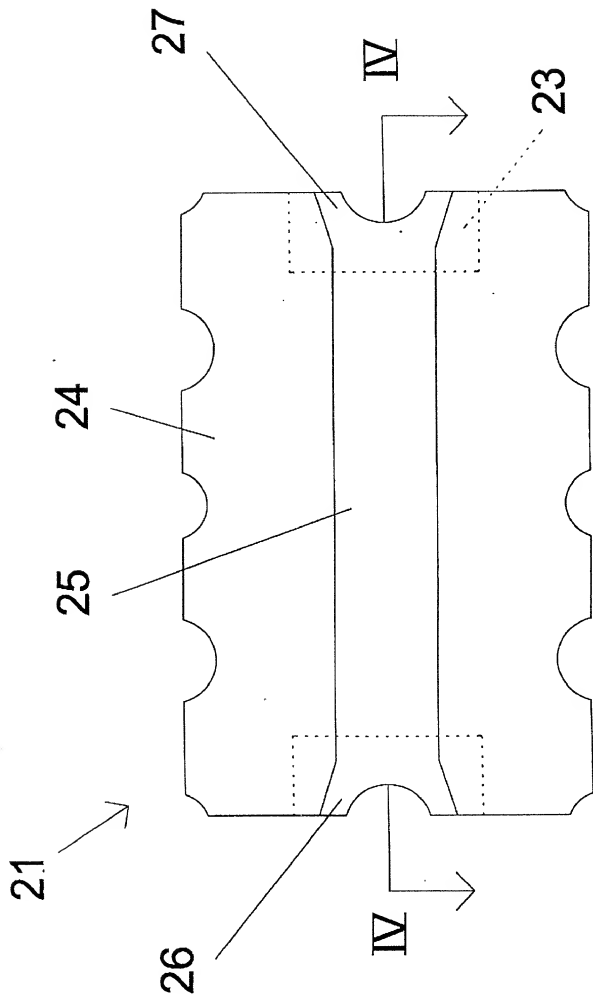


Figure 3

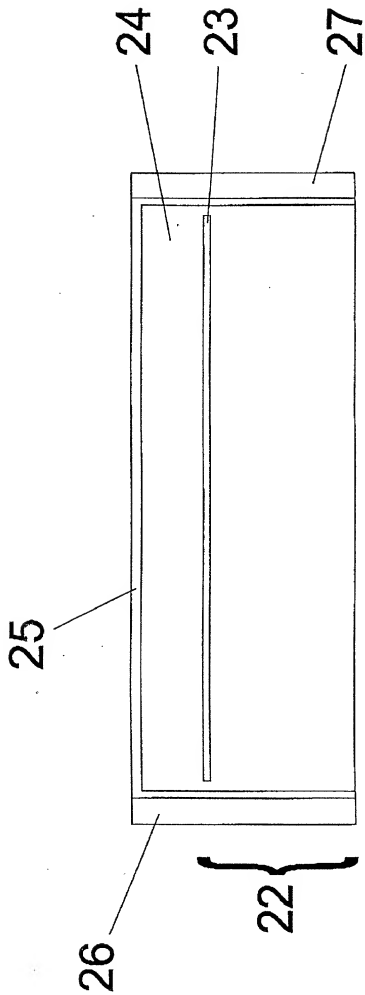


Figure 4

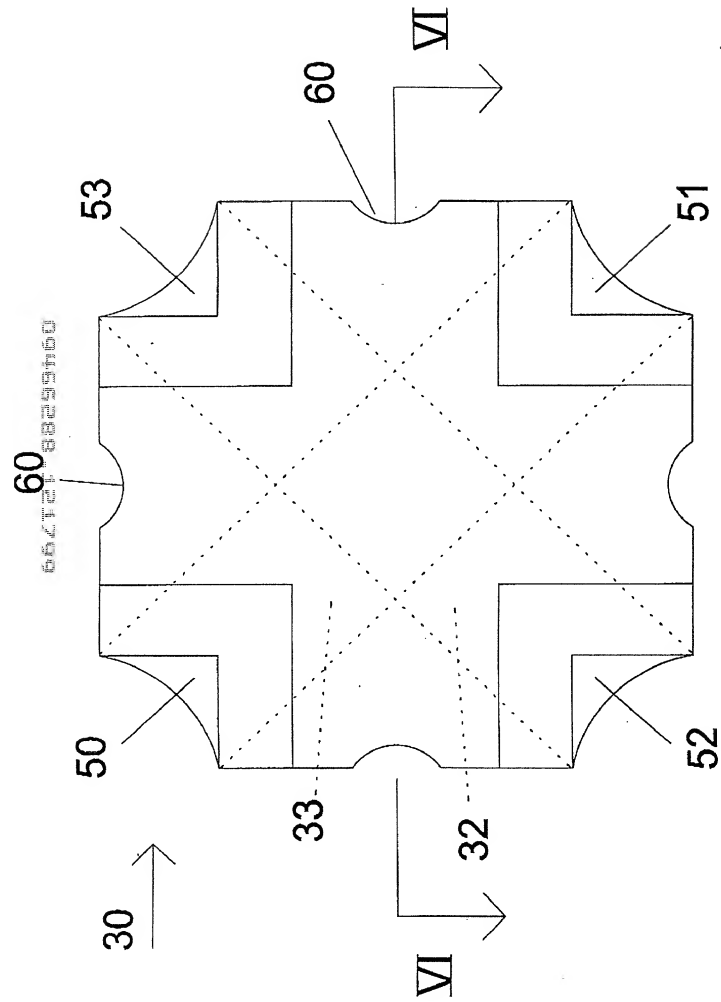
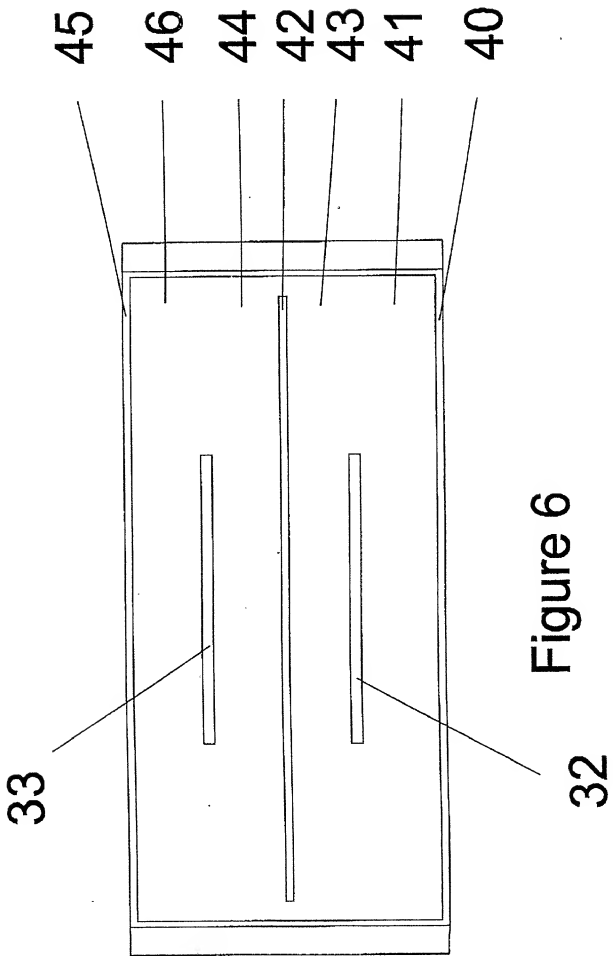
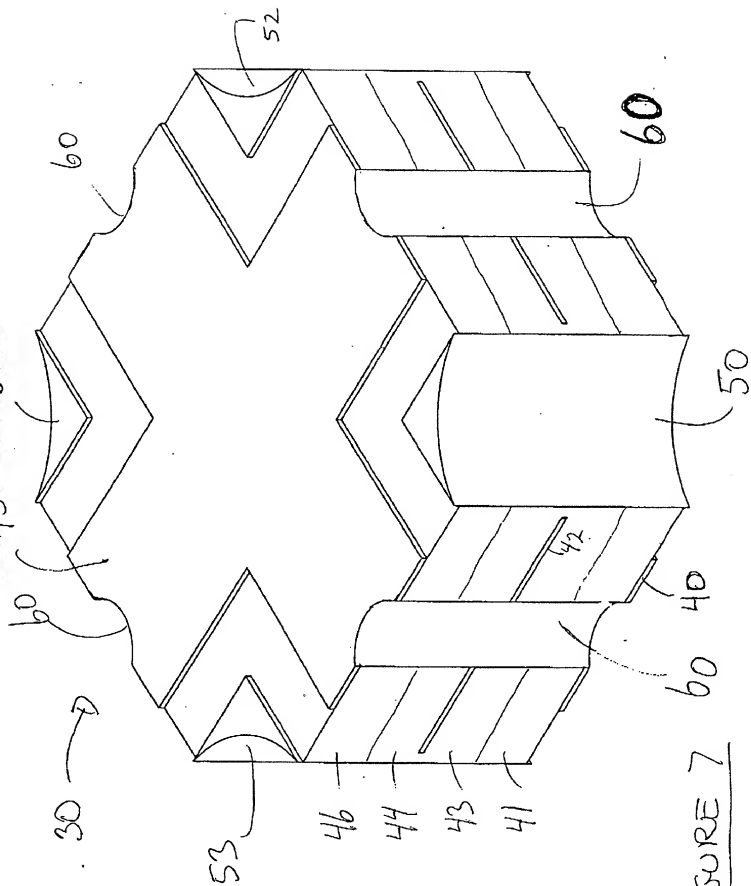


Figure 5





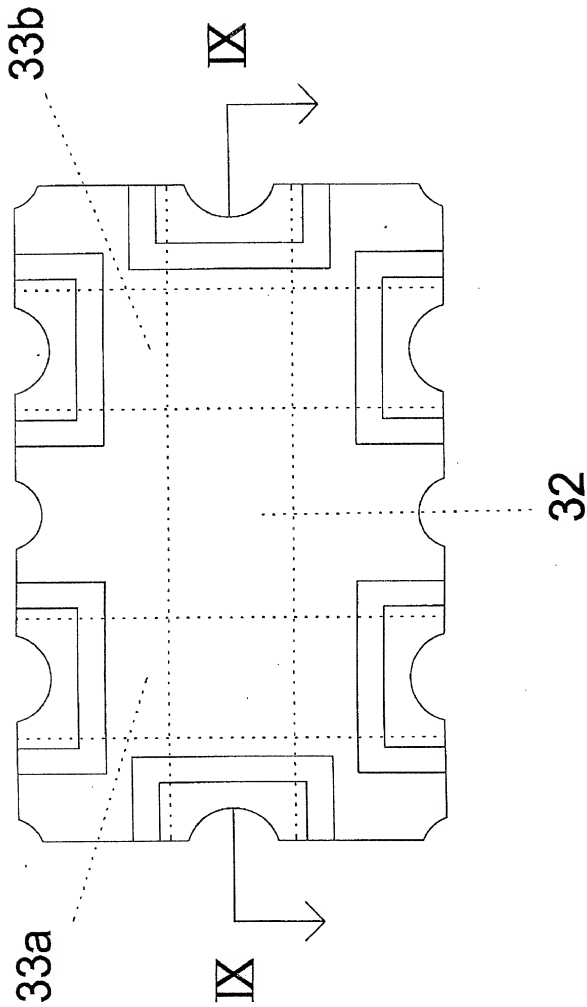


Figure 8

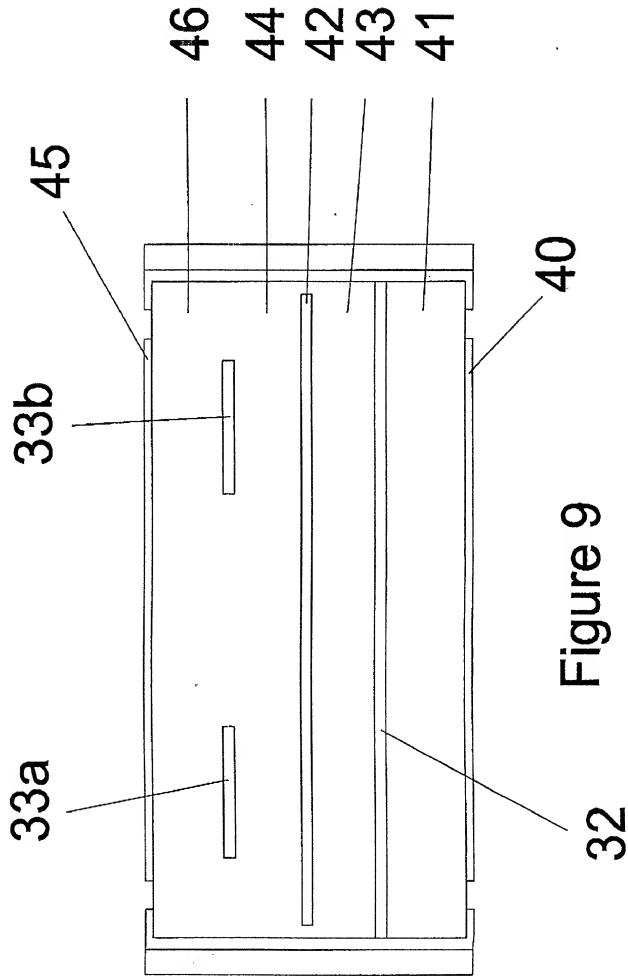


Figure 9

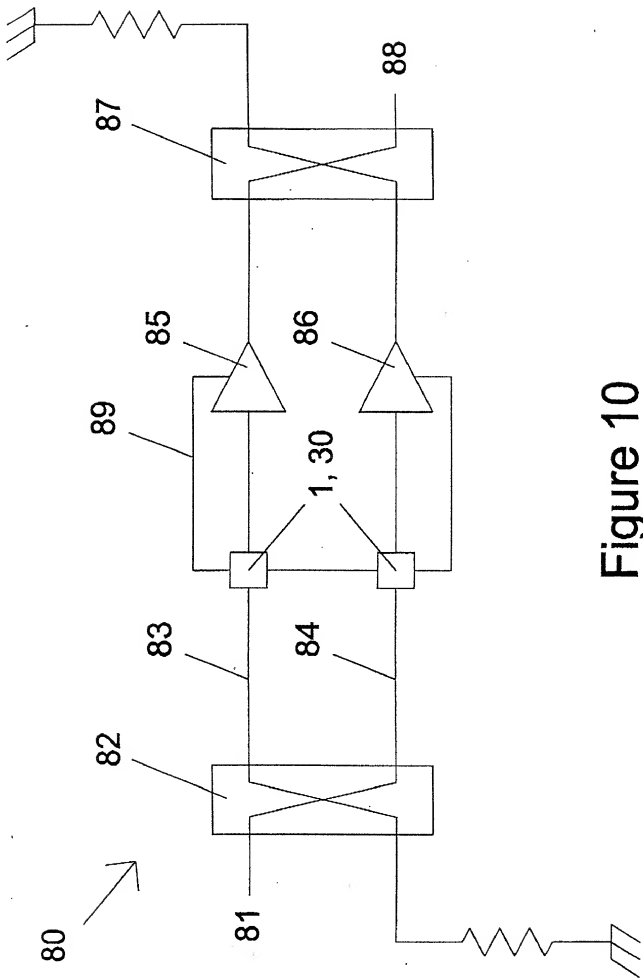


Figure 10

Docket No.
910_001**Declaration and Power of Attorney for Patent Application**
English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

A SURFACE MOUNT CROSSOVER COMPONENT

the specification of which

(check one)

- ☒ is attached hereto.
☐ was filed on _____ as United States Application No. _____ or PCT International Application Number _____ and was amended on _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International Application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT International Application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed

| | | | |
|-------------------|--------------------|---------------------------------|--------------------------|
| _____ (Number) | _____ (Country) | _____ (Day/Month/Year Filed) | <input type="checkbox"/> |
| _____ (Number) | _____ (Country) | _____ (Day/Month/Year Filed) | <input type="checkbox"/> |
| _____ (Number) | _____ (Country) | _____ (Day/Month/Year Filed) | <input type="checkbox"/> |

00465288-121799

I hereby claim the benefit under 35 U.S.C. Section 119(e) of any United States Provisional Application(s) listed below:

(Application Serial No.)

(Filing Date)

(Application Serial No.)

(Filing Date)

(Application Serial No.)

(Filing Date)

I hereby claim the benefit under 35 U.S.C. Section 120 of any United States Application(s), or Section 365(c) of any PCT International Application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International Application in the manner provided by the first paragraph of 35 U.S.C. Section 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, C.F.R. Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

(Application Serial No.)

(Filing Date)

(Status)
(patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status)
(patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status)
(patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.
(List names and Registration Number)

Thomas J. Wall, Reg. No. 24,280
Owen D. Marjama, Reg. No. 22,818
Peter J. Bilinski, Reg. No. 35,067
Stephen P. Burr, Reg. No. 32,970
George S. Blasiak, Reg. No. 37,283
Edward C. Jason, Reg. No. 26,743
Michael P. Williams, Reg. No. 43,271
Charu Narang Moody, Reg. No. P44,963
Alan F. Feeney, Reg. No. 43,609
Christopher R. Pastel, Reg. No. 37,694

Send Correspondence to:

Stephen P. Burr
WALL MARJAMA BILINSKI & BURR
101 South Salina Street, Suite 400
Syracuse, NY 13202

Direct Telephone Calls to: (name and telephone number)

Stephen P. Burr, (315) 425-9000

Full name of sole or first inventor

Hans Peter Ostergaard

Sole or first inventor's signature

Hans Peter Ostergaard

12-17-99 Date

Residence

Jamesville, NY 13078

Citizenship

Danish

Post Office Address

5195 Pointe East Drive

Jamesville, NY 13078

Full name of second inventor, if any

Second inventor's signature

Date

Residence

Citizenship

Post Office Address